

Appl. No. 10/649,230  
Amdt. dated March 9, 2006  
Reply to Office Action of January 10, 2006

PATENT

**Amendments to the Drawings:**

The attached sheets of drawings include changes to Figs. 1, 3, 5, and 7. These sheets, which include Figs. 1, 2, 3, 5 and 7, replace the original sheets including Figs. 1, 2, 3, 5 and 7.

Attachment: Replacement Sheet  
Annotated Sheet Showing Changes

**REMARKS/ARGUMENTS**

Claims 1-9 and 11 are pending in the present application. Claims 1 and 8 have been amended. No claims have been canceled, and no new claims have been added. Reconsideration is respectfully requested in light of the foregoing amendments and the following remarks.

**Claims**

Claim 1 has been objected to because "the emulation" on line 1 lacked antecedent basis. The claim has been amended as set forth above to overcome the claim objection.

In addition, claims 1 and 8 have amended to address minor grammatical matters.

**Drawing Objections**

a. Figs 1, 3 and 5 have been objected to because the blocks for "circuit boards" and "programmable logic circuits" should be so labeled. Applicant is submitting herewith corrected drawings, where short explanatory phrases have been added to explain the preexisting reference numerals in Figs 1, 3, and 5.

b. Fig. 7 has been objected to because, in accordance with the Office Action, all of the blocks of the flow chart need to be labeled according to their function. Applicant is submitting herewith corrected drawings where short explanatory phrases have been added to explain the preexisting alphabetical references in Fig 7.

**Correction to the Application Serial Number in the previously-filed Amendments**

Replacement pages 6-8 of the prior amendment are attached hereto, with the Application Serial No. in the header being corrected to 10/649,230.

Allowed Claims

Applicant appreciates that Claims 1-9 and 11 have been found allowable because the cited references do not teach or suggest a device for emulation of a design for integrated circuits in the manner as recited in the claims.

CONCLUSION

In view of the foregoing, Applicant believes all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 925-472-5000.

Respectfully submitted,



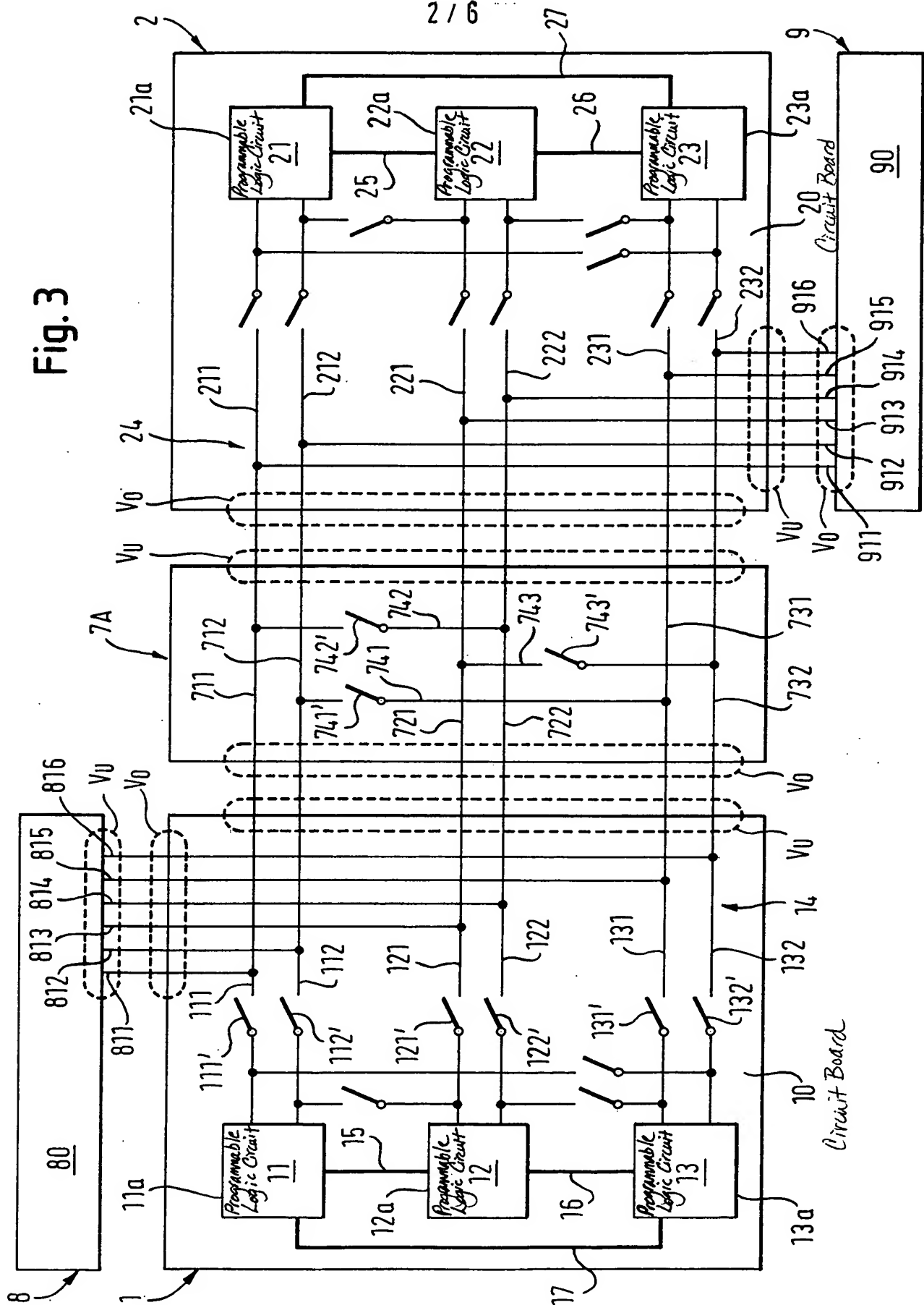
Babak Kusha  
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Attachments  
BK:lls  
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The diagram shows a switch assembly 141. A detailed view of the internal switch mechanism 141' is shown, which includes a series of switches labeled  $S_1, S_2, S_3, \dots, S_k$  connected in parallel between two main terminals.

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Fig. 3



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Fig. 5

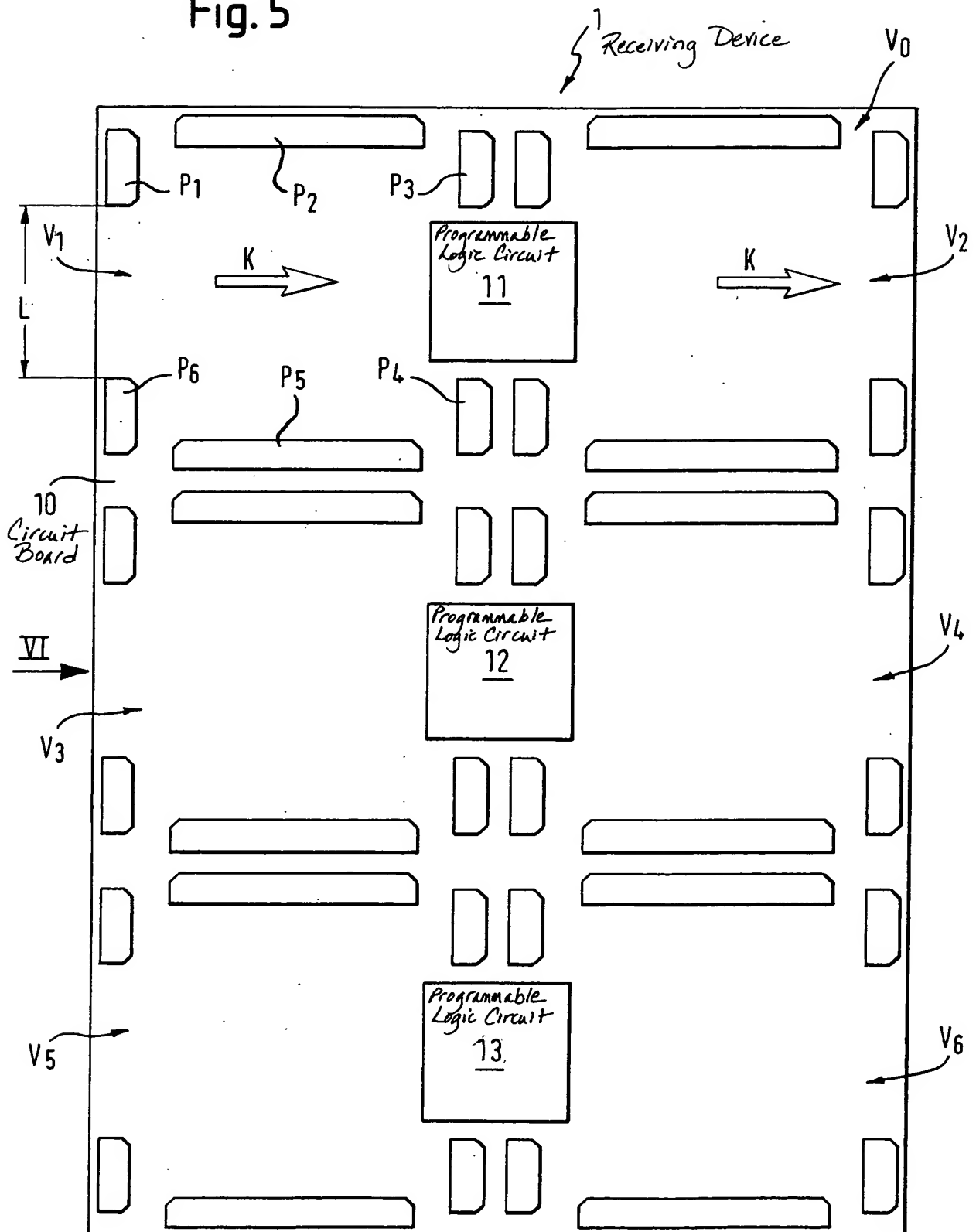


Fig. 7

